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[illegible]

Diagram illustrating a memory layout with 32-bit registers. The top two rows are empty. The third row contains the text "A=11 B=22 C=33" followed by "ABC" and a 7-bit register. The bottom row contains the text "112233" followed by a 25-bit register.

											
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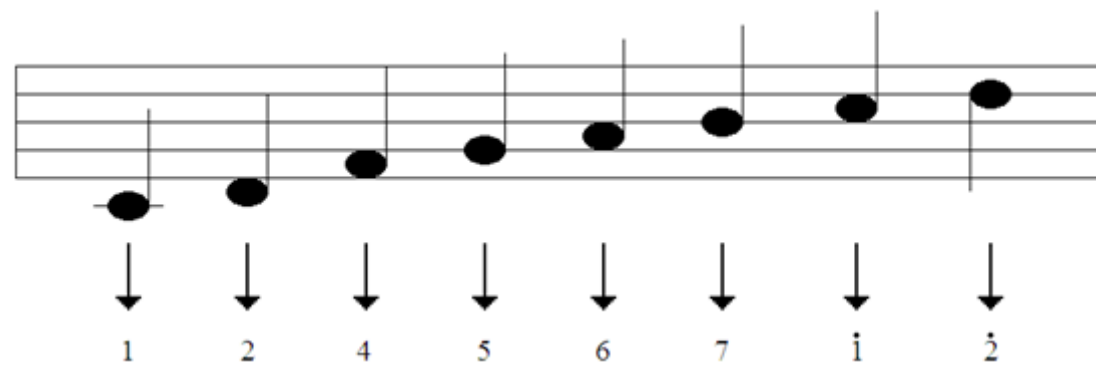
30

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A grid of 50 empty boxes for writing, arranged in 5 rows of 10.

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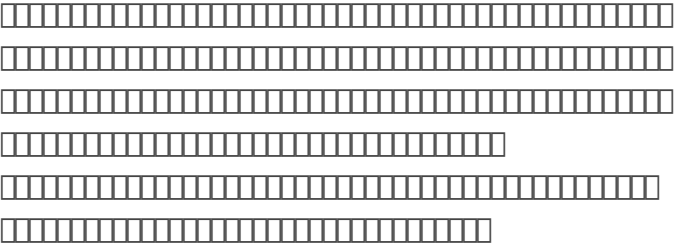


37

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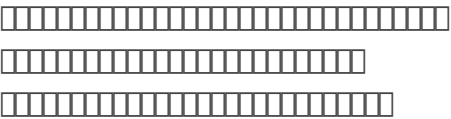
38



0— 1— 2— 3

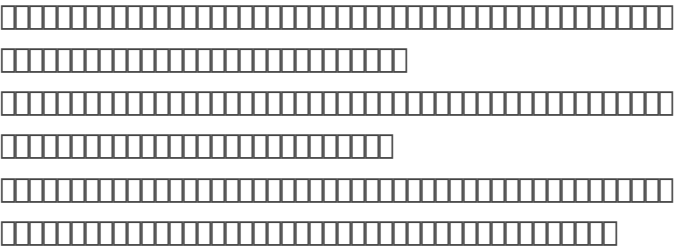
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- A 1x10 grid of small squares, arranged in 1 row and 10 columns.
- A 1x12 grid of small squares, arranged in 1 row and 12 columns.
- A 1x8 grid of small squares, arranged in 1 row and 8 columns.
- A 1x4 grid of small squares, arranged in 1 row and 4 columns.
- A 1x8 grid of small squares, arranged in 1 row and 8 columns.
- A 1x4 grid of small squares, arranged in 1 row and 4 columns.
- A 1x8 grid of small squares, arranged in 1 row and 8 columns.
- A 1x16 grid of small squares, arranged in 1 row and 16 columns.
- A 1x4 grid of small squares, arranged in 1 row and 4 columns.

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[illegible]

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